

Amendments to the Specification

Please replace the title with the following:

**IMAGE SENSOR CIRCUITS, SAMPLING CIRCUITS USED THEREIN AND
METHODS THEREFOR**

Please replace the paragraph beginning at page 12, line 7 with the following:

Beginning with the Reset Phase indicated in Figure 3, the photo sensing node 225 is charged by reset transistor 230 to a reset voltage level using voltage source V_{RD} . However, the actual voltage level achieved at the node 225 at the completion of the Reset Phase is in fact V_{RD} minus the feed through effect voltage of the reset transistor 230, V_{FT} . The resulting charged voltage level is referred to as the "reference black" voltage level ($V_{RB} = V_{DD} - V_{FT}$) ($V_{RB} = V_{RD} - V_{FT}$). This phase of the operation can also be referred to as a "pre-charge" stage.